			MIPI C-PHY Version & Adoption Date						
Category	**Feature**	**Mandatory / Optional**	**v1.0**	**v1.1**	**v1.2**	**v2.0**	**v2.1**	**v3.0**	
			4Q 2014	**1Q 2016**	**1Q 2017**	**3Q 2019**	**3Q 2021**	**1Q 2025**	
Encoding	Number of Wirestates	–	6	6	6	6	6	6	18
Symbol Rate (Gsps/Lane) & Data Rate (Gbps/Lane)	Standard Channel, Gsps *(Gbps)*	–	–	2.8 *(6.4)*	3.5 *(8.0)*	6.0 *(13.7)*	6.0 *(13.7)*	6.0 *(13.7)*	5.0 *(17.8)*
	Short Channel, Gsps *(Gbps)*	–	–	3.0 *(6.9)*	4.5 *(10.3)*	8.0 *(18.3)*	8.0 *(18.3)*	8.0 *(18.3)*	7.0 *(24.9)*
	Long Channel, Gsps *(Gbps)*	–	–	2.0 *(4.6)*	2.3 *(5.3)*	4.0 *(9.1)*	4.0 *(9.1)*	4.0 *(9.1)*	3.5 *(12.4)*
	Legacy Channel, Gsps *(Gbps)*	–	2.5 *(5.7)*	–	–	–	–	–	–
Pins	Minimum Pin Configuration	–	3	3	3	3	3	3	3
Increased Symbol Rate	Basic Pre-emphasis	O	–	✓	✓	✓	✓	✓	–
	Advanced TxEQ, Calibration, Additional UI (RCLK) Jitter specs	O	–	–	✓	✓	✓	✓	–
	Rx Equalization	O	–	–	–	✓	✓	✓	✓
	Multi-phase 18-state coding (3.556 coding factor)	M	–	–	–	–	–	–	✓
Power Reduction	Unterminated Mode	O	–	✓	✓	✓	✓	✓	–
	Reduced Amplitude “LVHS” Mode option	O	–	–	✓	✓	✓	✓	–
Low Power Mode	Legacy Low-Power Mode	M	✓	✓	✓	✓	✓	✓	–
	Alternate Low-Power Mode	O	–	–	✓	✓	✓	✓	✓
Enhanced Functions	16-bit/32-bit PPI	O	–	✓	✓	✓	✓	✓	✓
	64-bit PPI	O	–	–	–	–	✓	✓	✓
	Built-In Test	O	✓	✓	✓	✓	✓	✓	✓
	Optical Interconnect	O	–	✓	✓	✓	✓	✓	–
	High Speed Reverse Mode	O	–	–	–	✓	✓	✓	–
	Sync Word Selection to enhance Scrambling	O	–	–	✓	✓	✓	✓	✓
	PHY Generated/Detected Packet Delimiter	M	✓	✓	✓	✓	✓	✓	✓
	Fast Lane Turnaround	O	–	–	–	✓	✓	✓	–
	4m channel support, for IoT use cases	–	–	–	–	✓	✓	✓	✓
Protocol Specs	MIPI CSI-2, Version	–	v1.3	–	v2.0 / v2.1	v3.0	v4.0	v4.1	v4.1
	MIPI DSI-2, Version	–	–	v1.0	v1.1	v1.1	v2.0 – v2.2	v2.0 – v2.2	–