Enabling MIPI Camera Applications Including Automotive ADAS
MIPI Specifications in New Applications
Automotive, IoT / Wearables, Virtual / Augmented Reality
Camera Market Trends

• Higher refresh rates for HDR image quality
• Electronic eyes in consumer, IoT, surveillance
• AI imaging multiple image sensors in devices
• Always on sentinel camera use case
• Myriad of automotive image sensors
• Long reach cameras in IoT
Embedded Display Market Trends

- Higher resolutions: 4K 5.5” in mass market
- Mobile display innovation: folding displays
- Higher refresh rates, AR/VR 120Hz
- Future technologies drive bigger display sizes and higher resolutions
- Displays in automotive and industrial
New Requirements for Automotive Market
ISO 26262 Functional Safety Compliance and ASIL Certification

Electronics failure can have hazardous impact

Synopsys
MIPI Specifications for Automotive Applications

Automotive Infotainment System Diagram

- Head-up Display
- Storage
- Sensor
- SoundWire
- ECU
- CSU-2
- DS-2
- RF IC
- Radio-Frontend Module
- SoC
- Rear Seat Display
Example of MIPI In Automotive

MIPI CSI-2℠ Sensors & MIPI DSI℠ Display

- Proprietary Link (MIPI A-PHY in near future)

 MIPI CSI-2 Image Sensors

- Front Camera Module
- Left Camera Module
- Right Camera Module
- Rear Camera Module
- Other Camera Module

- Synopsys

- Display
- CAN Interface
- Power Supply
- MPU
- DRAM
- Flash Memory

- CAN Interface

© 2019 MIPI Alliance, Inc.
Asymmetric, high speed, low latency, low power, ‘safety critical’ links, spec available end/2019
MIPI Camera Serial Interface (CSI-2) Specification
De-facto standard interface used in car sensors
MIPI CSI-2 Evolution

- From mobile platform to imaging and vision

**Mobile**
- Human vision

**Imaging**
- Aggregator possibilities increased
- Beyond human vision

**Vision**
- Longer reach
- In-band control signaling

Synopsys
# MIPI CSI-2 Versions

- From Mobile to Imaging & Vision to Automotive

<table>
<thead>
<tr>
<th>CSI-2 Specification</th>
<th>v1.x</th>
<th>v2.x</th>
<th>v3.X</th>
<th>V4.X</th>
</tr>
</thead>
<tbody>
<tr>
<td>vX.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>29-Nov’05</td>
<td>28-Mar’17</td>
<td></td>
<td>10-Sep’19</td>
<td></td>
</tr>
<tr>
<td>• First release</td>
<td>• D-PHY 2v1 support</td>
<td>• D-PHY v2.5 support</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• MIPI C-PHY℠ 1v2 support</td>
<td>• C-PHY v2.0 support</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• RAW 16/20 &amp; DPCM 12-10-12</td>
<td>• RAW24</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• PPI width extension (up to 32)</td>
<td>• EoTp</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• LRTE</td>
<td>• USL</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Scrambling</td>
<td>• sROI</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>• Extension of Virtual Channel</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VX.1</td>
<td>09-Nov’10</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• MIPI D-PHY℠ 1.0 support</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VX.2</td>
<td>22-Jan’13</td>
<td>09-Apr’18</td>
<td></td>
<td></td>
</tr>
<tr>
<td>• D-PHY 1.0 support</td>
<td>• I3C SDR &amp; HDR_DRR support</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>VX.3</td>
<td>10-Sep’14</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>• D-PHY 1.2 support</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Target: Q4’19**
- Always ON Sentinel Controller (AOSC)
- Functional Safety (FuSa)
- Imaging Security (ISEC)
- Multi-Sensor Support
- A-PHY 1v0 support (through Link Layer)
MIPI CSI-2 Over MIPI D-PHY

![Diagram of MIPI CSI-2 over MIPI D-PHY](image)

**CSI-2 Host**
- Frame Buffer
- CSI-2 Receiver
  - Packet Decoder
  - Lane Merger
- MIPI CCI™ Master
  - SCL
  - SDA

**CSI-2 Device**
- Frame Buffer
- CSI-2 Transmitter
  - CNC Distribution
  - Packet Builder
- MIPI CCI℠ Slave
  - SCL
  - SDA

**Packet Builder**
- Virtual Channel Identification
- Data Format Definition
  - VC
  - DT
- Payload byte size
- Data CRC processing
  - CRC
- ECC protecting the header

**Data Format**
- Virtual Channel
- Data CRC processing
- ECC
- Payload byte size

**Payload Format**
- Data CRC processing
- ECC
- VC

**ECC**
- Protecting the header

**Virtual Channel**
- Identification

**Packet Format**
- CSI-2 Packet
- Lane Distribution
- D-PHY HS Burst
- CS-2 Packet
- Frame Buffer

**Definitions**
- Virtual Channel Identification
- Data Format Definition
- Packet Builder
- Data CRC processing
- ECC
- Payload byte size
RAW-16, RAW-20 & RAW-24 Color Depths

- CSI-2 v1.3 color depths are sufficient for Mobile applications, visible to human eye

- RAW-16/-20/-24 color depth bring advanced vision capabilities to Automotive and Industrial applications
  - Improves image capture when the environment changes suddenly and dramatically, for example in a big change in lighting condition

- Enable machines to make decisions from superior quality images
  - An autonomous vehicle, for example, could decipher whether darkness on an image is a harmless shadow or a pothole in the roadway to be avoided

Synopsys
Up to 32 Virtual Channels

- To accommodate the larger number of image sensors and their multiple data types
- To support multi-exposure & multi-range sensor fusion for ADAS
Added Latency Reduction & Transport Efficiency (LRTE)

- LRTE reduces frame transport latency & leakage power due to frequent “high speed - low power” transitions
- This enhances image sensor aggregation and multi exposure for real-time perception and decision making applications
Scrambling and New Compression Scheme

- Galois Field Scrambling reduces power spectral density (PSD) emissions
  - Minimizes PSD emissions from aggressor components, which are particularly beneficial when placed near sensitive receiver

- New DPCM 12-10-12 compression to further boost image quality
  - Superior SNR using reduced bandwidth PHY
  - Removes more compression artifacts when comparing with MIPI CSI-2 v1.3 compression mode

Synopsys
Smart Region of Interest (sROI)

- Delivering only data needed when it’s needed
- Reduces power consumption, enhances processing speed, solves bandwidth limitation, saving data storage space
- Improves image analysis, inferencing algorithms and making better deductions
  - Enable machines on a factory line to more quickly identify potential defects on a conveyor belt
  - Enable medical devices to recognize anomalies

Synopsys
MIPI D-PHY Architecture

The Popular Physical Layer for MIPI CSI-2 and DSI Protocols

- Synchronous Forwarded DDR clock link architecture
- One clock and multiple data lanes configuration
- Static/dynamic de-skew supported through calibration
- Calibration hand-shake not supported
- No encoding overhead
- Low-power and high-speed modes
- Primarily targeting camera and display
- Spread spectrum clocking supported for EMI/EMC considerations
- Large eco-system, proven in billions of phones and cars

Synopsys
MIPI C-PHY Architecture
Emerging Physical Layer for MIPI CSI-2 and DSI-2 Protocols

- Embedded clock enables assigning lanes
- Bit rate 2.28x the signaling rate, e.g. 1Gsym/s = 2.28Gb/s using encoding
- Multiple trio’s configuration to enable higher bandwidth
- Low-power and high-speed modes
- Low EMI/EMC considerations with embedded clock architecture
- Similar to D-PHY
  - LP (Low-Power) Mode is identical, functional definition & electrical specs
  - Common Channel models between the specs
  - PHY-Protocol Interface definition has a lot in-common
  - Similar High-Speed Mode voltage levels
  - A dual-mode C/D-PHY driver or receiver can be built to share the same pins enabling coexistence on same pins with existing D-PHY

Synopsys
Sensors in Autonomous Driving
Autonomous vehicles - Image sensor markets to 2027

- Standard saloon: Number of on-board image sensors 0
- 2016 top Advanced Driver Assistance System (ADAS) model: Number of on-board image sensors 11
- Fully automated driving (AD) vehicle: Number of on-board image sensors 25+

World market growth factor 2016-2026
- ADAS vehicles: 3.45
- Full AD vehicles: 161.4

Forecast demand AD/ADAS sensor types (million units)

<table>
<thead>
<tr>
<th>Year</th>
<th>Cameras</th>
<th>RADAR</th>
<th>LIDAR</th>
</tr>
</thead>
<tbody>
<tr>
<td>2016</td>
<td>52</td>
<td>13</td>
<td>1.6</td>
</tr>
<tr>
<td>2021</td>
<td>92</td>
<td>15</td>
<td>3.5</td>
</tr>
<tr>
<td>2030</td>
<td>400</td>
<td>40</td>
<td>50</td>
</tr>
</tbody>
</table>
# Overview of Sensor Technologies

<table>
<thead>
<tr>
<th>Feature Size</th>
<th>Mono Camera</th>
<th>Stereo Vision Camera</th>
<th>Trifocal Camera</th>
<th>Scanning/LiDAR</th>
<th>3D/ Flash LiDAR</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resolution of Feature Size</td>
<td>Weak</td>
<td>Good, but holes</td>
<td>Medium</td>
<td>Low</td>
<td>Dense Depth Map</td>
</tr>
<tr>
<td>Compute Processing</td>
<td>Low</td>
<td>Medium</td>
<td>Medium</td>
<td>High</td>
<td>High</td>
</tr>
<tr>
<td>Cost</td>
<td>Low</td>
<td>Low</td>
<td>Medium</td>
<td>Expensive</td>
<td>Medium</td>
</tr>
<tr>
<td>Configurability</td>
<td>Fixed</td>
<td>Fixed</td>
<td>Fixed</td>
<td>Flexible</td>
<td>Flexible</td>
</tr>
<tr>
<td>Distance Range</td>
<td>Long</td>
<td>Long</td>
<td>Long</td>
<td>Long</td>
<td>Medium</td>
</tr>
<tr>
<td>Low Light</td>
<td>Bad</td>
<td>Bad</td>
<td>Bad</td>
<td>Good</td>
<td>Good</td>
</tr>
<tr>
<td>Sunlight</td>
<td>Good</td>
<td>Good</td>
<td>Good</td>
<td>Good</td>
<td>Good</td>
</tr>
<tr>
<td>Snow, Fog, Rain</td>
<td>Bad</td>
<td>Bad</td>
<td>Bad</td>
<td>Medium</td>
<td>Medium</td>
</tr>
<tr>
<td>Accuracy</td>
<td>Medium</td>
<td>Good</td>
<td>Good</td>
<td>Good</td>
<td>Medium</td>
</tr>
<tr>
<td>Size</td>
<td>Small</td>
<td>Large</td>
<td>Large</td>
<td>Large</td>
<td>Small</td>
</tr>
</tbody>
</table>
Autonomous Cars Automation Levels

**LEVEL 0**
There are no autonomous features.

**LEVEL 1**
These cars can handle one task at a time, like automatic braking.

**LEVEL 2**
These cars would have at least two automated functions.

**LEVEL 3**
These cars handle "dynamic driving tasks" but might still need intervention.

**LEVEL 4**
These cars are officially driverless in certain environments.

**LEVEL 5**
These cars can operate entirely on their own without any driver presence.

SOURCE: SAE International
High Growth of Sensors in Cars
Over 20 Sensors Onboard

- **LIDARS**: High-precision laser sensors that detect fixed and moving objects.
- **CAMERAS**: Detect and track pedestrians/cyclists, traffic lights, free space, and other features.
- **ARTICULATING RADARS**: Detect moving vehicles at long range over a wide field of view.
- **LONG-RANGE RADARS**: Detect vehicles and measure velocity.
- **SHORT-RANGE RADARS**: Detect objects around the vehicle.
Today’s Image Sensor

<table>
<thead>
<tr>
<th>Feature</th>
<th>Level 3</th>
<th>Level 4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Camera Resolution</td>
<td>2-5MP</td>
<td>8MP</td>
</tr>
<tr>
<td># of cameras</td>
<td>6-12</td>
<td>12 or more</td>
</tr>
<tr>
<td>Smart Camera with AI built-in</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Road profile (lane, curb, surface, free space, etc.)</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Vehicle detection</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Pedestrian detection</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Traffic light recognition (RGY, turn light, tail light, etc.)</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Traffic sign recognition</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Obstacle detection (construction, animal, outliner, etc.)</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Driver monitoring</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Trifocal and surround based detection</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td>Surround view</td>
<td>✔</td>
<td>✔</td>
</tr>
</tbody>
</table>
Challenges on Current Sensor Technology

• Bandwidth
  – Camera Resolution continues to increase, so as data bandwidth
  – LiDAR needs large data bandwidth

• Distributed networks versus centralized networks

• Real time update (low latency)

• Harsh weather conditions: rain, snow, fog (moisture)

• Redundancy for higher functional safety requirement (ASIL D)

Synopsys
Future Proof MIPI A-PHY to Meet Increased BW

- High bandwidth gears with vision to support 24-48Gb/s+
- Camera aggregation use case
- Flexibility in supporting distributed networks
- High bandwidth display interconnect

Synopsys
Meeting Automotive Requirements
Key Requirements of Automotive-Grade IP

- Reduce Risk & Accelerate Qualification for Automotive SoCs

- **Functional Safety**: Accelerate ISO 26262 functional safety assessments to help ensure designers reach target ASIL levels

- **Reliability**: Reduce risk & development time for AEC-Q100 qualification of SoCs

- **Quality**: Meet quality levels required for automotive applications

*Synopsys*
ADAS Domain Controller SoC Architecture

- Image sensor/radar/lidar/night vision
- CSI-2 Aggregator
- Sensor
- CSI-2
- I3C
- UFS
- Ethernet
- A-PHY
- DSI
- eDP
- HDMI
- Display
- ECU
- Network
- Storage
Automotive Safety Features

- DesignWare MIPI CSI-2 Device Controller IP

- Built-in interrupt injection
- Built-in error injection for Memories
- Interfaces and RAM protected by Parity
- ECC protection on data stored in memory

Certification for ISO 26262 Part 5 HW
Synopsys MIPI IP for Automotive

- DesignWare MIPI Controllers & PHYs for Sensors, ADAS, and Infotainment SoCs
- ISO 26262 certified* IP helps accelerate SoC-level qualification & reach target ASILs
- AEC-Q100 designed and tested MIPI IP meets Grade 1 & 2 temperature requirements
- Quality management system meets automotive quality requirements

- MIPI PHYs
  - Functional Safety ISO 26262 ASIL B*
  - AEC-Q100 Grade1 and Grade2 options
  - Quality DFMEA
- CSI-2 and DSI controllers
  - Functional Safety ISO 26262 ASIL B*
  - Quality DFMEA

* Certification of IP for ISO 26262 Chapter 5 random HW failures
Hangzhou FABU Tech co. LTD And Synopsys

FABU Leverages Synopsys’ DesignWare MIPI IP to interface with high-definition cameras and displays
DesignWare IP Solutions for Cameras, Displays, Sensors

Single-Vendor Solution, Production-Proven in >1B units, Interoperable eco-system

- Complete end-to-end solution scaling to meet all project needs
- Optimized to deliver best power & area
- Production & interoperability proven to reduce risk
- Lowest integration effort to accelerate time-to-market
- Available silicon-proven MIPI PHYs in mainstream and advanced FinFET processes
Thank You
ADDITIONAL RESOURCES

- www.synopsys.com/mipi